

# 2SK2936

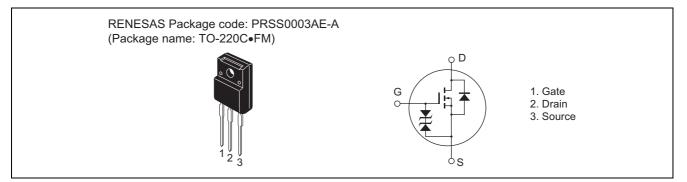
Silicon N Channel MOS FET High Speed Power Switching

> REJ03G1050-0400 (Previous: ADE-208-559B) Rev.4.00 Sep 07, 2005

## Features

- Low on-resistance  $R_{DS} = 0.010 \Omega$  typ.
- High speed switching
- 4 V gate drive device can be driven from 5 V source

### Outline





# Absolute Maximum Ratings

			$(Ta = 25^{\circ}C)$
Item	Symbol	Ratings	Unit
Drain to source voltage	V <sub>DSS</sub>	60	V
Gate to source voltage	V <sub>GSS</sub>	±20	V
Drain current	ID	45	A
Drain peak current	I <sub>D(pulse)</sub> Note1	180	A
Body-drain diode reverse drain current	I <sub>DR</sub>	45	A
Avalanche current	I <sub>AP</sub> Note3	45	A
Avalanche energy	E <sub>AR</sub> <sup>Note3</sup>	173	mJ
Channel dissipation	Pch Note2	35	W
Channel temperature	Tch	150	°C
Storage temperature	Tstg	–55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1 %

2. Value at Tc = 25°C

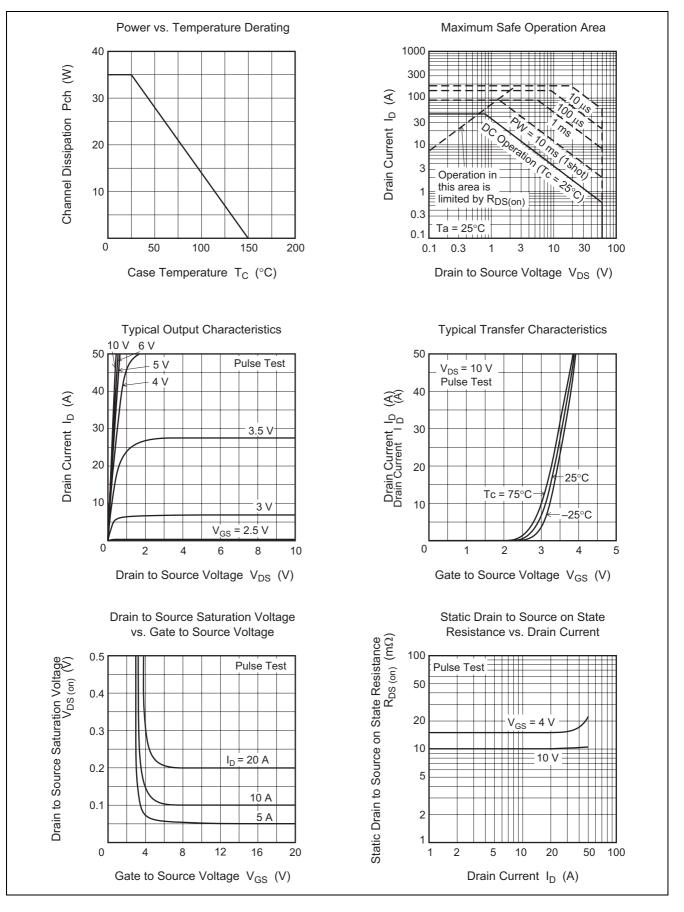
3. Value at Tch = 25°C, Rg  $\geq$  50  $\Omega$ 

### **Electrical Characteristics**

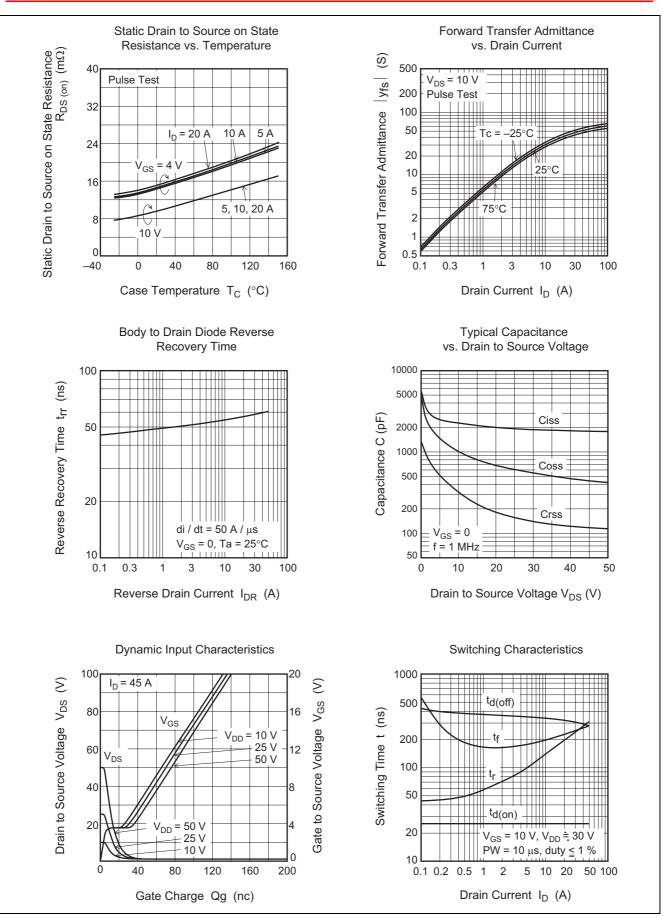
						$(Ta = 25^{\circ}C)$	
Item	Symbol	Min	Тур	Max	Unit	Test Conditions	
Drain to source breakdown voltage	V <sub>(BR)DSS</sub>	60	—	_	V	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0	
Gate to source breakdown voltage	V <sub>(BR)GSS</sub>	±20	—	_	V	$I_{G} = \pm 100 \ \mu A, V_{DS} = 0$	
Gate to source leak current	I <sub>GSS</sub>	_	—	±10	μA	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$	
Zero gate voltage drain current	I <sub>DSS</sub>	_	—	10	μA	$V_{DS} = 60 V, V_{GS} = 0$	
Gate to source cutoff voltage	V <sub>GS(off)</sub>	1.5	—	2.5	V	I <sub>D</sub> = 1 mA, V <sub>DS</sub> = 10 V	
Static drain to source on state	R <sub>DS(on)</sub>	_	0.010	0.013	Ω	$I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}^{\text{Note4}}$	
resistance	R <sub>DS(on)</sub>	_	0.015	0.025	Ω	$I_D = 20 \text{ A}, V_{GS} = 4 \text{ V}^{\text{Note4}}$	
Forward transfer admittance	y <sub>fs</sub>	24	40	_	S	$I_D = 20 \text{ A}, V_{DS} = 10 \text{ V}^{\text{Note4}}$	
Input capacitance	Ciss		2200	_	pF	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0, f = 1 MHz	
Output capacitance	Coss		1050	_	pF		
Reverse transfer capacitance	Crss		320		pF	1	
Turn-on delay time	t <sub>d(on)</sub>	_	25	_	ns	I <sub>D</sub> = 20 A, V <sub>GS</sub> = 10 V	
Rise time	tr		200		ns	$V_{GS} = 10 \text{ V}, \text{ I}_D = 20 \text{ A},$ $R_L = 1.5\Omega$	
Turn-off delay time	t <sub>d(off)</sub>		320		ns		
Fall time	t <sub>f</sub>	_	240	_	ns		
Body–drain diode forward voltage	V <sub>DF</sub>	_	0.95	_	V	I <sub>F</sub> = 45A, V <sub>GS</sub> = 0	
Body–drain diode reverse	trr	_	60	_	ns	I <sub>F</sub> = 45A, V <sub>GS</sub> = 0	
recovery time						di <sub>F</sub> / dt = 50 A/ μs	

Note: 4. Pulse test

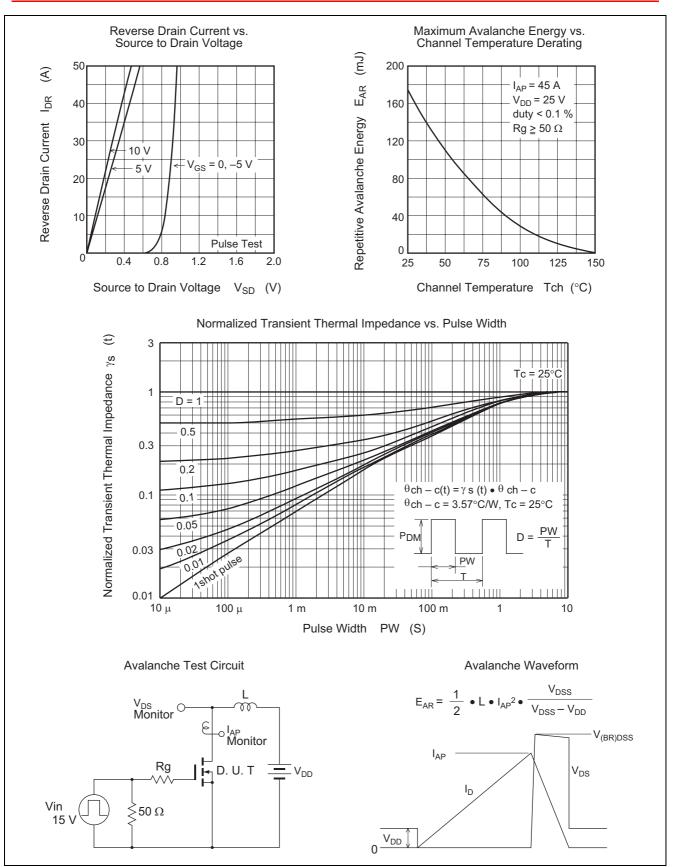
### **Main Characteristics**



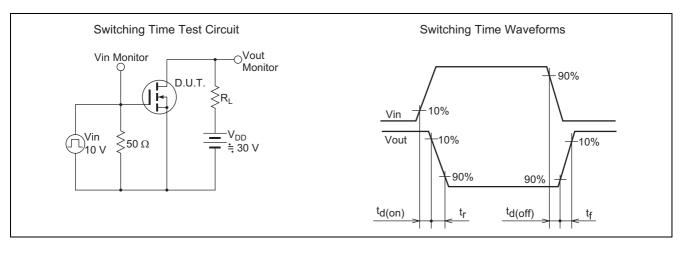






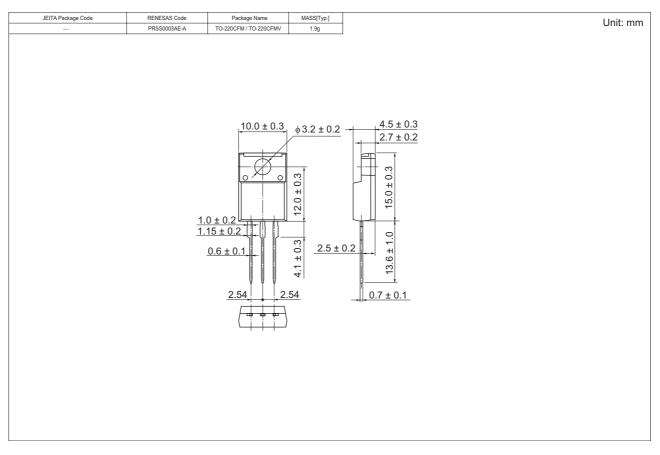








# Package Dimensions



## **Ordering Information**

Part Name	Quantity	Shipping Container
2SK2936-E	600 pcs	Box (Tube)

Note: For some grades, production may be terminated. Please contact the Renesas sales office to check the state of production before ordering the product.



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